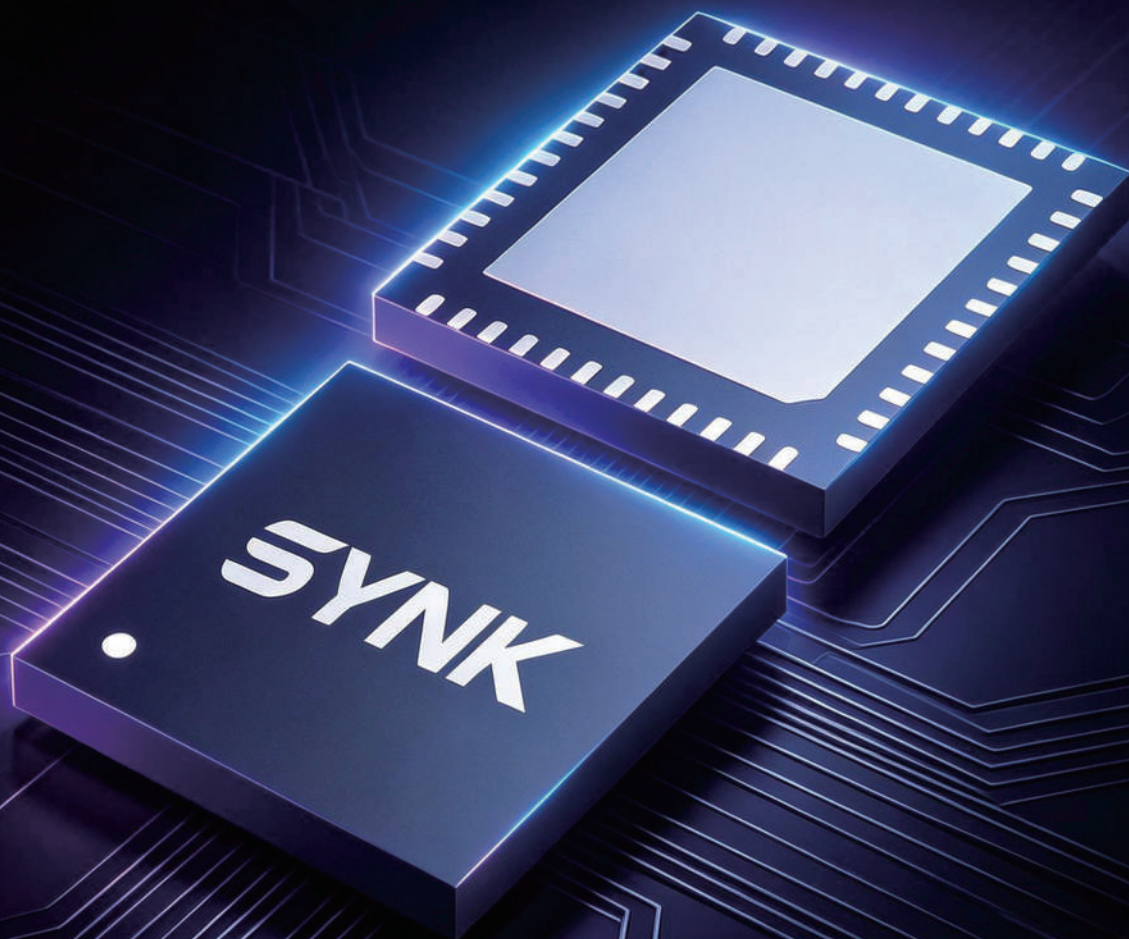


时钟芯片解决方案

一站式高性能时钟芯片方案，为数字系统提供精准的“心脏”跳动



数模混合设计核心技术
Core technology in analog-digital hybrid design



超低相位抖动 (230fs@156.25M)
Ultra-low phase jitter (230fs @ 156.25 M)



多协议兼容PCIe 1.0~7.0
Multi-protocol compatibility:
PCIe 1.0~7.0



Pin to Pin 兼容大部分主流型号
Pin-to-pin compatible with most mainstream models

多兼容 Broad Compatibility

PCIe 1.0~6.0 compliant, supports CC/SRnS/SRIS

灵活配置 Flexible Configuration

I²C/OTP/pin-strapping control modes

1MHz to 333.33MHz, 1/2/4/9/10/19 clocks output

Per-output control of I/O type, voltage, and drive strength

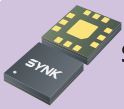
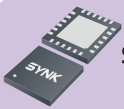
超低抖动 Ultra-Low Jitter

230fs RMS jitter @ 156.25MHz (Typ)

Widely used in HPC, SSD, Ethernet, optical modules, data centers, GPU, CPU, and more.



5G

Series	Package	Supply Voltage	Diff. Output Frequency	Specifications	Pin to Pin
 SYKG1011E	12-pin LGA	1.8/2.5/3.3V	1~333.33MHz	1 diff. (LVDS/HCSL/LP_HCSL)+1 LVCMOS	New
 SYKG1011Q5	12-pin LGA	1.8/2.5/3.3V	1~333.33MHz	1 diff. (LVDS/HCSL/LP_HCSL)+1 LVCMOS	New
  SYKG1021E	16-pin LGA	1.8/2.5/3.3V	1~333.33MHz	2 diff. (LVDS/HCSL/LP_HCSL)+1 LVCMOS	9FGV1005C 9FGV1006C 9FGV1008C
 SYKG1021Q5	16-pin LGA	1.8/2.5/3.3V	1~333.33MHz	2 diff. (LVDS/HCSL/LP_HCSL)+1 LVCMOS	9FGV1005CQ5 9FGV1006CQ5 9FGV1008CQ5
  SYKG1042E	24-pin VFQFN	1.8/2.5/3.3V	1~333.33MHz	4 diff. (LVDS/HCSL/LP_HCSL)+2 LVCMOS	9FGV1001C 9FGV1002C 9FGV1004C
 SYKG1042Q5	24-pin LGA	1.8/2.5/3.3V	1~333.33MHz	4 diff. (LVDS/HCSL/LP_HCSL)+2 LVCMOS	9FGV1001CQ5 9FGV1002CQ5 9FGV1004CQ5
 SYKG6965	24-pin QFN	1.8/2.5/3.3V	1~300MHz	4 diff. (LVDS/HCSL/LP_HCSL)+1 LVCMOS	5P49V6965
 SYKG1100E	64-pin QFN	1.8/2.5/3.3V	10~1.2GHz	10 diff. (LVDS/HCSL/LVPECL/CML) or 20 LVCMOS	Si5345
 SYKG1090E	48-pin LGA	3.3V	100MHz	9 diff. LP-HCSL	Si52147
 SYKG1200E	100-pin QFN	3.3V	25MHz 100MHz	19 diff. LP-HCSL	9SQ440 PI6CG330440



无毛刺切换 Glitch-Free Switching

Seamless reference clock switching

每路独立控制 Per-Output Control

I/O type, voltage, and enable independent per output

多兼容 Broad Compatibility

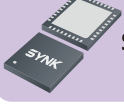
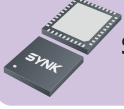
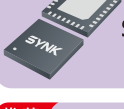
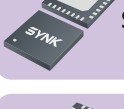
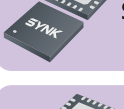
3.1GHz differential input, up to 10 differential outputs
PCIe 1.0~6.0 compliant

高 PSRR High PSRR

-53dBc @ 156.25MHz (Typ)

Widely used in servers, CPU, GPU, DPU, ADC/DAC, data centers, and more.



Series	Package	Supply Voltage	Diff. Output Frequency	Specifications	Pin to Pin
推荐  SYKB23F10(G)	48-pin WQFN	1.8 ~ 3.3 V	LP_HCSL: DC~1GHz LVPECL/LVDS: DC~3.1GHz LVCMOS: DC~350MHz	10 diff. (LVDS/LVPECL/LP_HCSL) + 1 LVCMOS	LMK00301 8T39S11A
 SYKB23F08(G)	40-pin WQFN	1.8 ~ 3.3 V	LP_HCSL: DC~1GHz LVPECL/LVDS: DC~3.1GHz LVCMOS: DC~350MHz	8 diff. (LVDS/LVPECL/LP_HCSL) + 1 LVCMOS	LMK00308 8T39S08A
 SYKB23F06(G)	36-pin WQFN	1.8 ~ 3.3 V	LP_HCSL: DC~1GHz LVPECL/LVDS: DC~3.1GHz LVCMOS: DC~350MHz	6 diff. (LVDS/LVPECL/LP_HCSL) + 1 LVCMOS	LMK00306 8T39S06A
推荐  SYKB23F04(G)	32-pin WQFN	1.8 ~ 3.3 V	LP_HCSL: DC~1GHz LVPECL/LVDS: DC~3.1GHz LVCMOS: DC~350MHz	4 diff. (LVDS/LVPECL/LP_HCSL) + 1 LVCMOS	LMK00304 8T39S04A
 SYKB23H08(G)	40-pin WQFN	1.8 ~ 3.3 V	DC~1GHz	8 diff. LP_HCSL + 1 LVCMOS	LMK00338
 SYKB23H04(G)	32-pin WQFN	1.8 ~ 3.3 V	DC~1GHz	4 diff. LP_HCSL + 1 LVCMOS	LMK00334
 SYKB13C10(G)	32-pin WQFN	1.5 ~ 3.3 V	DC~350MHz	10 single-ended CMOS outputs	8L30110
推荐  SYKB23C10(G)	32-pin WQFN	1.5 ~ 3.3 V	DC~350MHz	10 single-ended CMOS outputs	LMK00101
 SYKB22C05(G)	24-pin WQFN	1.5 ~ 3.3 V	DC~350MHz	5 single-ended CMOS outputs	LMK00105 8L30205
 SYKB22M05(G)	24-pin WQFN	1.5 ~ 3.3 V	LP_HCSL: DC~1GHz LVPECL/LVDS: DC~3.1GHz LVCMOS: DC~350MHz	2 diff. (LVDS/LVPECL/LP_HCSL) + 3 LVCMOS	New

Note: "G" indicates that the clock buffer supports glitch-free reference clock switching.

PCle Gen7 合规 PCIe Gen7 compliant

Meets reference clock requirements for PCIe 1.0 ~ 7.0, backward compatible.

超多路输出 Ultra-high output count

Provides 8/12/16/20 LP-HCSL outputs for synchronizing numerous devices.

灵活耦合输入 Flexible input coupling

Supports HCSL, LVDS, AC-coupled, and DC-coupled inputs, with input frequency from 1 MHz to 400 MHz.

无毛刺切换 Glitch-free switching

Highly reliable systems for hot swapping of primary and backup clocks.

Widely used in server motherboards, PCIe riser cards, GPU clusters, NVLink switches, and data center backplanes.



Series	Package	Supply Voltage	Diff. Output Frequency	Specifications	Pin to Pin
 SYKBD20AK/F	72-pin QFN	1.8V/3.3V	1 ~ 400 MHz	20 outputs LP-HCSL PCIe Gen7	RC19020A072 9QXL2000 ZL40292 PI6CB332000
推荐  SYKBD20ALK/F	80-pin GQFN	1.8V/3.3V	1 ~ 400 MHz	20 outputs LP-HCSL Thin package PCIe Gen7	RC19020 9QXL2001C LMKDB1120 ZL40294 PI6CB332001A
 SYKBD20ALGK/F	80-pin GQFN	1.8V/3.3V	1 ~ 400 MHz	20 outputs LP-HCSL Glitch-free switching PCIe Gen7 Thin package	New
 SYKBD16AK/F	64-pin VFPQFN	1.8V/3.3V	1 ~ 400 MHz	16 outputs LP-HCSL PCIe Gen7	RC19016
 SYKBD12AK/F	64-pin LGA	1.8V/3.3V	1 ~ 400 MHz	12 outputs LP-HCSL PCIe Gen7	9QXL1200
 SYKBD12AGK/F	64-pin LGA	1.8V/3.3V	1 ~ 400 MHz	12 outputs LP-HCSL Glitch-free switching PCIe Gen7 Thin package	New
 SYKBD08AK/F	40-pin VFPQFN	1.8V/3.3V	1 ~ 400 MHz	8 outputs LP-HCSL PCIe Gen7	RC19008 LMKDB1108